

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination KIM ET AL.	
	10/067,265	Examiner	Art Unit Asok K. Sarkar

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,150,263	11-2000	Lin et al.	438/652
	B	US-6,458,284	10-2002	Kashihara	216/47
	C	US-2002/0187434	12-2002	Blatchford et al.	430/322
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber, "Silicon processing for the VLSI Era, Vol. 1", Chapter 16, p 539, Lattice Press, CA (1986).
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.